

#2
7-28-00
PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: John H. Green, et al.

Serial No.: To be assigned

Dated: May 18, 2000

Filed: Herewith

Docket:
13498 (CA19990031US1)

1c525 U.S. PTO
09/574409
05/19/00

For: LOOP ALLOCATION FOR OPTIMIZING
COMPILERS

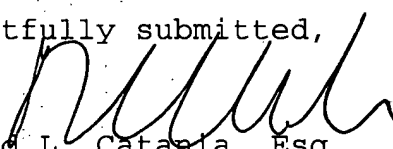
Assistant Commissioner for Patents
Washington, DC 20231

CLAIM OF PRIORITY

Sir:

Applicant in the above-identified application hereby
claims the right of priority in connection with Title 35 U.S.C.
§119 and in support thereof, herewith submits a certified copy of
Canadian Patent Application 2,290,167 filed on November 22, 1999.

Respectfully submitted,


Richard L. Catania, Esq.
Registration No. 32,608

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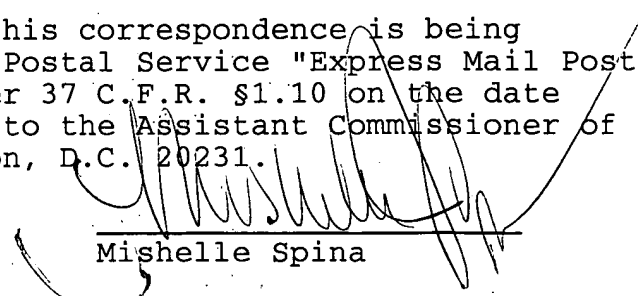
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Dated: May 18, 2000


Michelle Spina



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ments déposés au Bureau des brevets.

This is to certify that the documents
attached hereto and identified below are
true copies of the documents on file in
the Patent Office.

Specification and Drawings, as originally filed, with Application for Patent Serial No:
2,290,167, on November 22, 1999, by **IBM CANADA LIMITED - IBM CANADA
LIMITEE**, assignee of John H. Green, Sandeep K. Minocha, Piotr Przybylski and John W.
Stephenson, for "Automated Interface Generation for Computer Programs in Different
Environments".


Agent certificateur/Certifying Officer

January 10, 2000

Date

Canada

(CIPO 68)

OPIC



CIPO